

Docket No.: WMP-EUP-008

JUL 20 2004

2818

[Signature]

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By: _____

Date: July 15, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/056,770 Confirmation No: 4597
Applicant : Gottfried Ferber et al.
Filed : January 24, 2002
Title : Semiconductor Module and Method for Fabricating the
Semiconductor Module
Art Unit : 2818
Examiner : Long K. Tran
Docket No. : WMP-EUP-008
Customer No. : 24131

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. 1.97(i)

Hon. Commissioner for Patents

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 5,730,543 (Schonauer et al.), dated March 24, 1998, and corresponding International Publication WO 95/18777, dated July 13, 1995;

United States Patent Application Publication US 2002/0033529 A1 (Ferber et al.), dated March 21, 2002, and corresponding International Publication WO 00/55917, dated September 21, 2000;

German Patent De 32 41 508 C2 (Leukel et al.), dated May 10, 1984, and English abstract thereof;

German Published Non-Prosecuted Patent Application DE 195 27 611 A1 (Maier et al.), dated January 30, 1997, substrate and method for producing it;

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German Examination report dated August 12, 2003.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

It is believed that the enclosed prior art is less pertinent than the prior art previously submitted and cited by the Examiner. Kindly place the references in the Patent Office file wrapper.

Respectfully submitted,



For Applicants

RALPH E. LUCHEN
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Date: July 15, 2004

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